

Direct conversion for FLEX paging receivers - A feasibility study (1998 [RFIC])

Zhiheng Chen and J. Lau. "Direct conversion for FLEX paging receivers - A feasibility study (1998 [RFIC])." 1998 Radio Frequency Integrated Circuits (RFIC) Symposium 98. (1998 [RFIC]): 293-296.

To achieve an integrated solution for the 6400 bps FLEX paging receiver, we examined the direct conversion architecture and addressed two major problems that may obstruct the implementation: (1) the compensation of the DC offset, and (2) the design of a 4-level FSK demodulator at zero-IF. Simulation results show that the combination of harmonic mixers with a zero-crossing-interpolation based demodulator is a possible solution.

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